

FORM PTO-1449
U.S. Department of Commerce Patent and Trademark Office

Docket No. (Optional)
CIT1270-1

Serial No.:
09/905,157

Applicant(s):
Lewis, et al.

INFORMATION DISCLOSURE STATEMENT
BY APPLICANT

Filing Date:
July 12, 2001

Group Art Unit:
2811

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U.S. PATENT DOCUMENTS

EXAM. INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
EK	3,832,700	08/27/1974	Wu and Francombe			
EK	4,106,951	08/15/1978	James V. Masi			
EK	4,461,691	07/24/1984	Arthur J. Frank			
EK	4,608,097	08/26/1986	Weinberger et al.			
EK	5,429,708	07/04/1995	Linford and Chidsey			
EK	5,599,742	02/04/1997	Shingo Kadomura			
EK	5,972,724	10/26/1999	Arndt et al.			
EK	6,147,405	11/14/2000	Yongjun Jeff Hu			
EK	6,197,668 B1	03/06/2001	Gardner and Gilmer			

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EXAM. INITIALS	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION (YES/NO)
EK	JP54-76629	06/19/1979	Japan			Abstract
EK	JP63-161672	07/05/1988	Japan			Abstract
EK	DE 42 29 031 A1	03/03/1994	Germany			Abstract
EK	JP6-84852	03/25/1994	Japan			Abstract

EXAMINER <i>Erik Kish</i>	DATE CONSIDERED <i>10/9/02</i>
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages)

EK		Ikawa et al., "A New Surface Protecting Layer for Si Substrate Preventing Surface Oxidation," J. Electrochem. Soc., Vol. 138, No. 8 pgs. 2382-2386, August 1991.
EK		Wolf, Stanley and Tauber, Richard N., "Silicon Processing for the VLSI Era: Process Technology," Lattice Press: Sunset Beach, CA. pgs. 264-267 (1986).



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